



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Whitman et al.

Serial No.: 09/997,019

Filed: November 28, 2001

For: SPIN COATING FOR MAXIMUM
FILL CHARACTERISTIC YIELDING A
PLANARIZED THIN FILM SURFACE

Confirmation No.: 6139

Examiner: B. Kebede

Group Art Unit: 2823

Attorney Docket No.: 2269-4294.2US
(98-1208.02/US)

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P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 6,251,487	June 26, 2001	Yonaha

Other Documents

WOLF, STANLEY AND TAUBER, RICHARD, Silicon Processing for the VLSI Era, Volume 1: Process Technology, pp. 429-434, Lattice Press, Sunset Beach , California 1986

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application.

Respectfully submitted,



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Date: November 5, 2004
BGP/nj:rh

Enclosures: Form PTO-1449 or PTO/SB/08
Copy of documents cited

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	09/997,019
Filing Date	November 28, 2001
First Named Inventor	Whitman et al.
Group Art Unit	2823
Examiner Name	B. Kebede
Attorney Docket Number	2269-4294.2US (98-1208.02/US)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]Examiner
SignatureDate
Considered

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¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08B(10-03)

Approved for use through 7/31/2006. OMB 0651-0031

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 2 of 2

Complete if Known

Application Number	09/997,019
Filing Date	November 28, 2001
First Named Inventor	Whitman et al.
Group Art Unit	2823
Examiner Name	B. Kebede
Attorney Docket Number	2269-4294 21JS (98-1208 02/1JS)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		WOLF, STANLEY AND TAUBER, RICHARD, Silicon Processing for the VLSI Era, Volume 1: Process Technology, pp. 429-434, Lattice Press, Sunset Beach, California 1986	

Examiner Signature		Date Considered	
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